

FIG. 1 PRIOR ART

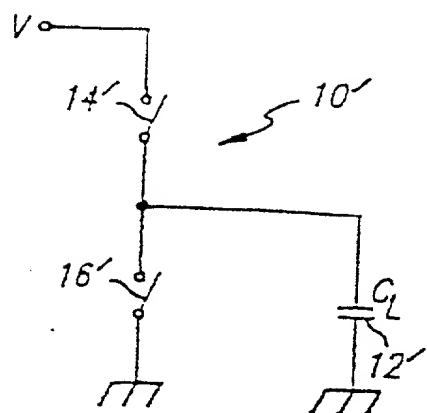


FIG. 2 PRIOR ART

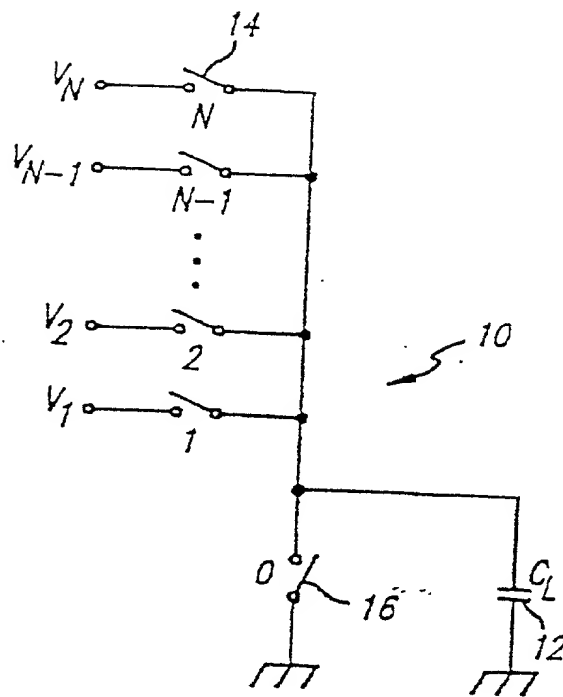
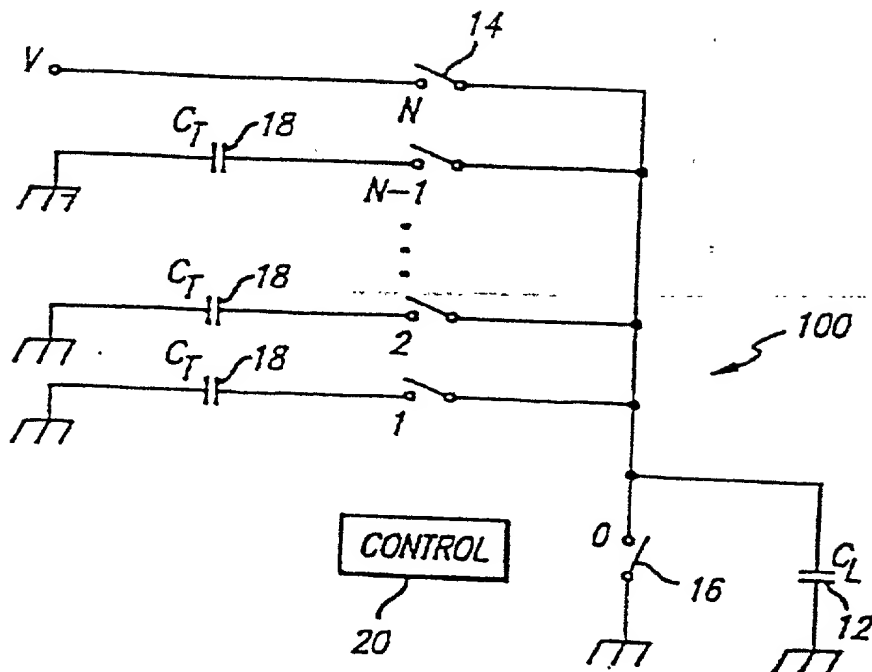


FIG. 3



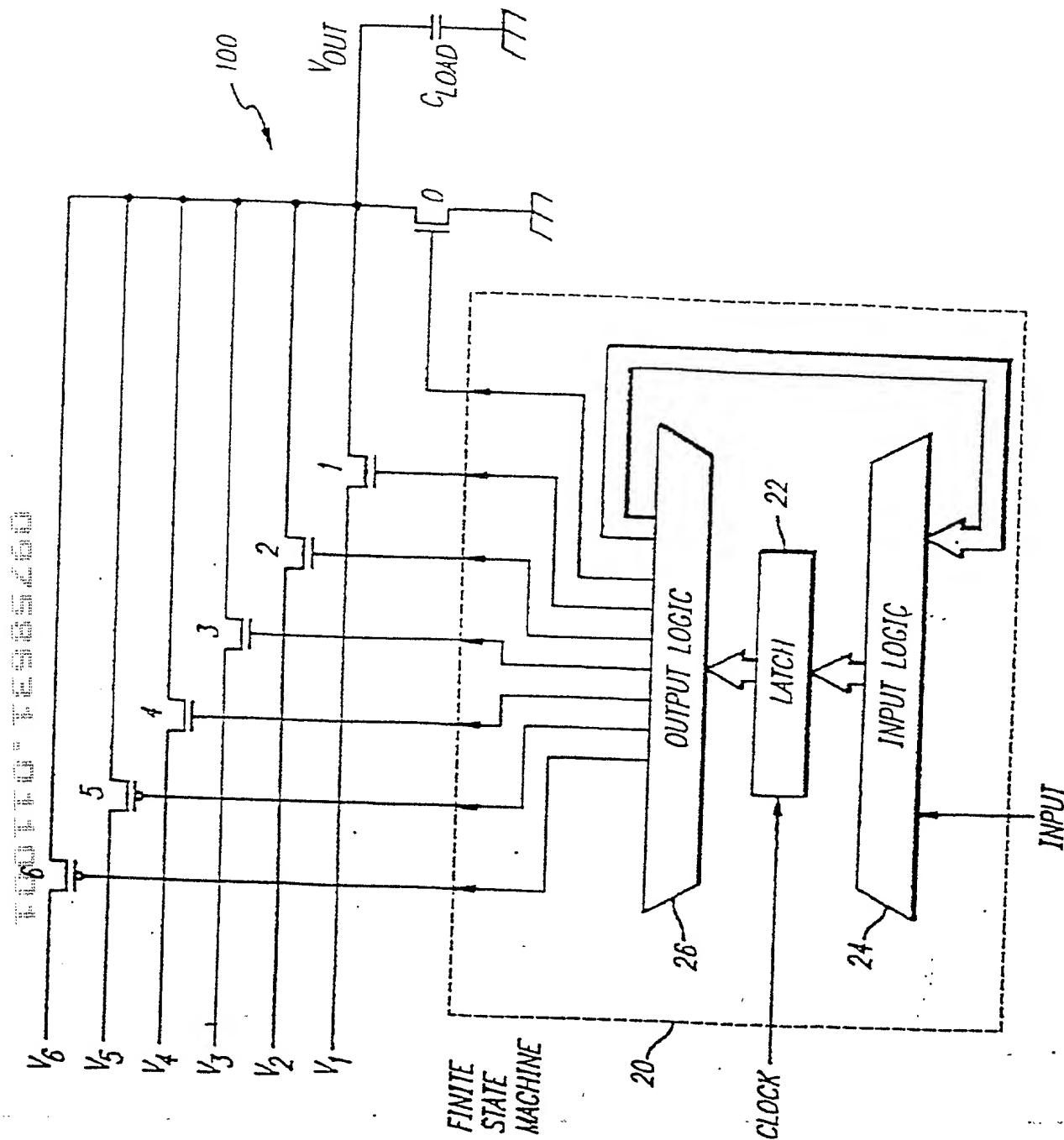


FIG. 4

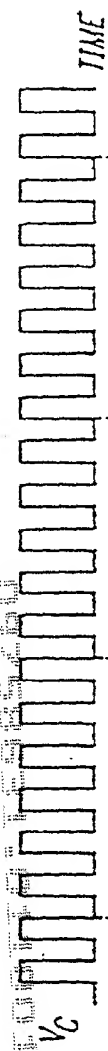


FIG. 5(a) CLOCK

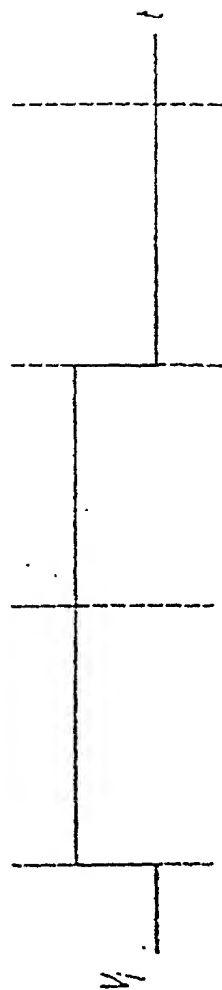


FIG. 5(b) INPUT

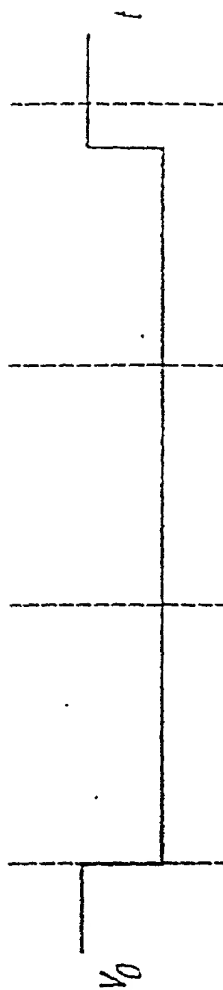


FIG. 5(c) SWITCH #0 CONTROL

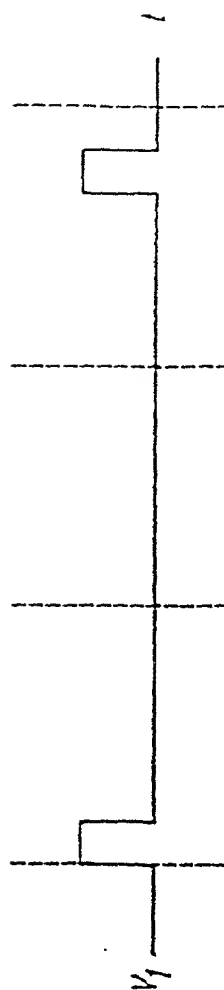


FIG. 5(d) SWITCH #1 CONTROL

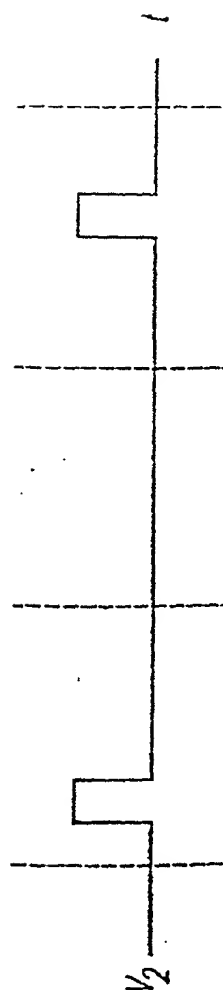


FIG. 5(e) SWITCH #2 CONTROL

FIG. 5(f) - 1995-12-05

FIG. 5(f)

SWITCH
#3 CONTROL

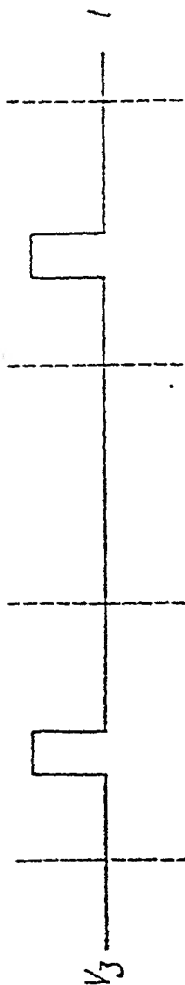


FIG. 5(g)

SWITCH
#4 CONTROL

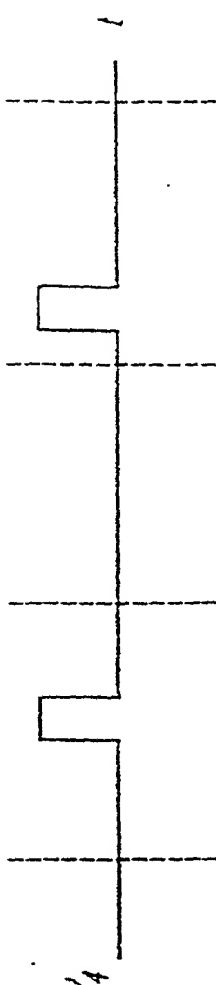


FIG. 5(h)

SWITCH
#5 CONTROL

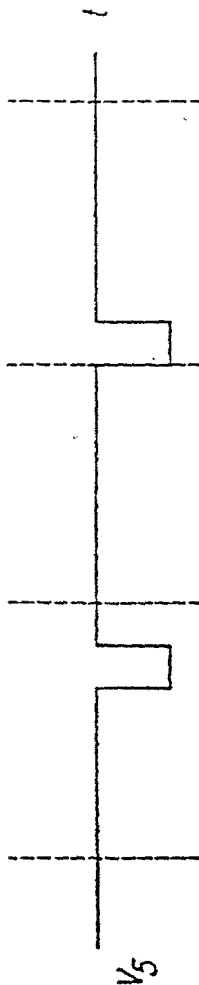


FIG. 5(i)

SWITCH
#6 CONTROL

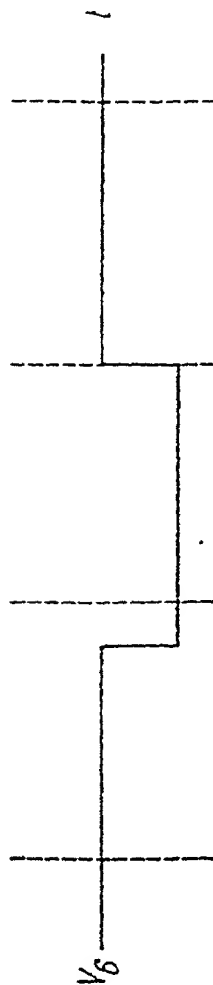


FIG. 5(j)

OUTPUT

